

SWARNANDHRA COLLEGE OF ENGINEERING & TECHNOLOGY

ACCREDITED NATIONAL BOARD OF ACCREDITATION, AICTE, NEW DELHI

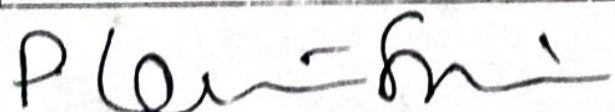
ACCREDITED BY NAAC WITH "A" GRADE – 3.32/4 CGPA.

Recognized under 2(f) & 12(B) of UGC Act 1956, Approved by AICTE, Permanently Affiliated to JNTUK, Kakinada
SEETHARAMPURAM, NARSAPUR-534280. W.G.DT.,

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

BOARD OF STUDIES FOR UG

S.No.	Name	Designation	Designation	Remarks
1	Prof.P.Lakshmi Sarojini	Professor	Head of the Department, ECE, SCET, Narasapur.	Chairman
2	Mr. N.V.B.Gangadhar Rao	Associate Professor	Dept. of ECE, SCET, Narasapur	Internal Member
3	Mr.N.Srikanth	Associate Professor	Dept. of ECE, SCET, Narasapur	Internal Member
4	Mr.K.V.B.Chandra Sekhar Rao	Associate Professor	Dept. of ECE, SCET, Narasapur	Internal Member
5	Ms.M.Bhavani	Associate Professor	Dept. of ECE, SCET, Narasapur	Internal Member
6	Dr.G.Sasi Bhushan Rao	Professor	Dept. of ECE, Andhra University. Visakhapatnam	Subject Expert Nominated by V.C
7	Dr. K.Sarath Kumar	Professor	Dept. of ECE, KLU, Vaddeswaram, Guntur	Subject Expert Nominated by A.C
8	Dr. M.Kamaraju	Professor	Dept. of ECE, GEC, Gudlavalleru, Krishna Dt.	Subject Expert Nominated by A.C
9	Mr.V.Govinda Rao	Managing Director	M/S Microlink Peripheral Controls Pvt. Ltd., Vijayawada.	Industrial Expert
10	Mr.M.Satyanarayana	Physical Design Engineering	Socpronics Technoligies Pvt. Ltd., Hyderabad.	Alumni Member



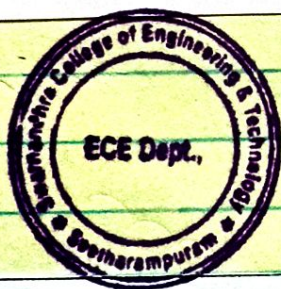
HEAD OF THE DEPARTMENT


PRINCIPAL

BOARD OF STUDIES MEETING

Agenda of first Board of Studies Meeting of Department of Electronics & Communication Engineering, Swarnandhra College of Engineering & Technology (Autonomous) on 31-08-2014 at 11:00 AM.

1. Constitution of Board of Studies
2. Introduction of the Members of Board of Studies
3. Autonomy at Department level.
4. Views on Academic Regulations – Course Structure – Autonomous.
5. Finalization of I & II Sem., subjects syllabus of respective departments from Academic Year 2014-15.
6. Finalization of Sem. wise course structure i.e. 1st to 8th Semester for the admitted batch of students from the Academic Year 2014-15.
7. Employability Skills among the students-Improvement.
8. Industry, Institute interaction-Suggestions for Excellence.
9. Any other matter with the permission of chair.



P. Lakshmi Sarojini

**CHAIRMAN
BOARD OF STUDIES
Department of ECE
Swarnandhra College of Engineering & Technology**

BOARD OF STUDIES FOR UG

The following BOS members are present for the meeting held on 31-08-2014 at 11:00 AM for UG Programme.

S.No.	Name of BOS Member	Designation in BOS	Signature
1	Prof.P.Lakshmi Sarojini	Chairman	<i>P. Lakshmi Sarojini</i>
2	Mr. N.V.B.Gangadhar Rao	Internal Member	<i>N.V.B. Gangadhar Rao</i>
3	Mr.N.Srikanth	Internal Member	<i>N. Srikanth</i>
4	Mr.K.V.B.Chandra Sekhar Rao	Internal Member	<i>K.V.B. Chandra Sekhar Rao</i>
5	Ms.M.Bhavani	Internal Member	<i>M. Bhavani</i>
6	Dr.G.Sasi Bhushan Rao	Subject Expert Nominated by V.C	<i>G. Sasi Bhushan Rao</i>
7	Dr. K.Sarath Kumar	Subject Expert Nominated by A.C	<i>K. Sarath Kumar</i>
8	Dr. M.Kamaraju	Subject Expert Nominated by A.C	<i>M. Kamaraju</i>
9	Mr.V.Govinda Rao	Industrial Expert	<i>V. Govinda Rao</i>
10	Mr.M.Satyanarayana	Alumni Member	<i>M. Satyanarayana</i>

**DEPARTMENT
OF
ELECTRONICS & COMMUNICATION ENGINEERING**

MINUTES OF MEETING BOARD OF STUDIES

Minutes Of First Board Of Studies Meeting Of Department Of
Electronics & Communication Engineering, Swarnandhra College Of
Engineering & Technology (Autonomous) On 31-08-2014 At 11.00 AM.

1. The Board of studies for department of ECE for Swarnandhra College Of Engineering & Technology (Autonomous) is constituted and made functional with the nominees of VC, JNTUK, Kakinada and Academic Council.
2. The members of BOS have been introduced by the chairman to the other members.
3. To implement autonomy at the department level from the academic year 2014-2015, based on the necessary permission granted by UGC Lr No. F.22-1/2014(AC), dated 19-6-2014 and JNTU/DAB/B1/Swarnandhra autonomous/2014 dt 23-08-2014.
4. To adopt the academic regulations that are formulated and after the approval of the contents of academic regulations by Academic Council for all the semesters of ECE-UG Programme.
5. It is also advised to adopt the model question papers for each semester according to the suggestions given by the members of the BOS.
6. It is advised that the list of the names of Panel of Examiners are discussed and proposed for the final approval of Academic Council.

UG PROGRAMME:

1. It is advised to change the subject names as per recent trends of industry like EDC as Design of Electronic systems. (Applications to be introduced) in the IIIrd Sem so that syllabus is application oriented.
2. It is advised that the Signals & Systems subject to include topics from signal concept to Z-Transform, Exclude Z-Transforms in DSP and include latest processors in DSP in last 2 units.
3. It is advised to change EDC Lab as "Design of Electronic Systems Lab" in IIIrd Semester.
4. It is suggested to change the EC & PDC Lab names as Electronic Design & Testing lab.

5. It is resolved to change the subject named VLSI Design as "Design of VLSI Systems" as per industry expectations.
6. In Microprocessor & Micro Controllers subject more emphasis is to be made on Micro-Controllers with Real Time Based Applications.
7. In VIIth Semester , subject " Embedded Systems" is changed to Embedded Systems Design subject to approval.
8. To Include CSE Subjects like Cloud Computing, Big Data & SDR (Software Defined Radio) in the curriculam.Replace TV Engineering subject as SDR (Software Defined Radio).
9. The subject named "Random Variables & Stochastic Processing" is split into Random Variables & Stochastic processing.

Random variables topic can be covered in Mathematics of communication Engineering subject along with complex variables and probability theory.

Stochastic processing can be included along with Analog Communications subject in Essentials of Analog Communications.



P. Velupillai Srinivasan
CHAIRMAN
BOARD OF STUDIES
Department of ECE
Swamandhra College of Engineering & Technology